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Patent Application  
Attorney Docket No.: 57941.000063  
Client Reference No.: RA001.2003.2.C.US

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:

Michael FARMWALD et al.

Appln. No.: 10/716,596

Filed: November 20, 2003

For: INTEGRATED CIRCUIT I/O USING  
A HIGH PERFORMANCE BUS  
INTERFACE

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: Group Art Unit: 2818  
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: Examiner: Unassigned  
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**Mail Stop Amendment**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Sir:

In accordance with the duty under 37 C.F.R. § 1.56 of each individual associated with the filing and prosecution of the above-identified patent application (hereinafter, "associated individuals") to disclose all information known to that individual to be material to patentability, Applicant(s) hereby submits attached Form PTO-1449 (modified) listing cited references. This submission is made in accordance with 37 C.F.R. §§ 1.97 and 1.98 and M.P.E.P. § 609.

The cited references, while believed to be of some relevance, are not necessarily considered to teach or suggest any aspect of the invention described and claimed in the above-identified patent application. Applicant(s) hereby expressly

reserves the right to swear behind the effective dates of any of the cited references. Applicant(s) further reserves the right to question the relevance, materiality, and/or prior art status of any of the cited references in whole, in part, or in combination, subsequent to the filing of this information disclosure statement. This information disclosure statement is also not to be construed as a representation that a search has, or has not, been conducted or that no better art exists. Rather, this information disclosure statement discloses only the best references of which the associated individuals are aware.

The Examiner is respectfully requested to consider each of the cited references, to indicate such consideration by initialing in the space provided next to each cited reference on the enclosed Form PTO-1449 (modified), to sign the initialed Form PTO-1449 (modified), and to return a copy of the same with the next communication to the Applicant(s).

In accordance with 37 C.F.R. § 1.98(a), only copies of the cited references which are not U.S. patents or U.S. patent application publications are being submitted herewith. However, copies of the cited references which are U.S. patents or U.S. patent application publications will be submitted at the request of the Examiner. In considering these cited references, it may be noted by the Examiner that certain of the references may

contain markings, underlinings, and/or other notations. These markings, underlinings, and/or other notations are not to be construed as drawing the Examiner's attention either to selected parts or away from other parts of these cited references. Any such markings were either present on the copies of these cited references when obtained by the associated individuals, or were made thereon during the study of these cited references by the associated individuals.

In accordance with 37 C.F.R. § 1.97(b), this information disclosure statement is being filed (i) within three months of the filing date of the above-identified patent application; (ii) within three months of the date upon which the above-identified patent application entered the national stage as set forth in 37 C.F.R. § 1.491; (iii) before the mailing date of a first Office Action on the merits for the above-identified patent application; or (iv) before the mailing date of a first Office Action after the filing of a request for continued examination under 37 C.F.R. § 1.114. Accordingly, no statement or fee is required.

Please charge any shortage in fees due in connection with the filing of this communication to Deposit Account No. 50-0206, and please credit any excess fees to such deposit account.

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Respectfully submitted,

Hunton & Williams LLP

By: 

Thomas E. Anderson

Registration No. 37,063

TEA/sdw

Hunton & Williams LLP  
1900 K Street, N.W.  
Washington, D.C. 20006-1109  
Telephone: (202) 955-1500  
Facsimile: (202) 778-2201

Date: January 21, 2005

FORM PTO-1449  
(REV. 7-80)U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.:  
57941.000063SERIAL NO.:  
10/716,596INVENTOR'S NAME:  
Michael FARMWALD et al.EXAMINER:  
UnassignedFILING DATE:  
November 20, 2003GROUP:  
2818

## LIST OF MATERIALS CITED BY APPLICANT

(Use several sheets if necessary)

## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	1.	4360870	11/23/1982	McVey			
	2.	4379222	04/05/1983	Hayter et al.			
	3.	4394753	07/19/1983	Penzel			
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	9.	4597019	06/24/1986	Nishimoto et al.			
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EXAMINER

DATE CONSIDERED

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

FORM PTO-1449 (REV. 7-80)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO.: 57941.000063	SERIAL NO.: 10/716,596
		INVENTOR'S NAME: Michael FARMWALD et al.	EXAMINER: Unassigned
		FILING DATE: November 20, 2003	GROUP: 2818
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31.	Johnson, Mark G; Hudson, Edwin L., IEEE Journal of Solid-State Circuits, Vol. 23, No. 5, 10/00/1988, Johnson (IEEE 10/88), A Variable Delay Line PLL For CPU- Coprocessor Synchronization		
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				<b>FILING DATE:</b> November 20, 2003	<b>GROUP:</b> 2818		
<b>OTHER MATERIALS (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							
	49.	Lidington, Gary P., Digital Technical Journal, No. 7, August 1988, pp. 79-86, Overview of the MicroVAX 3500/3600 Processor Module					
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